

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of

Takashi TERAUCHI, et al.

Serial No.: 09/976,341

Filed: October 15, 2001

Customer Number: 20277

Confirmation Number: 9211

Group Art Unit: 2812

Examiner: R.E. Pompey

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, AND  
SEMICONDUCTOR DEVICE HAVING MEMORY CELL

**NOTICE OF APPEAL**  
**FROM THE PRIMARY EXAMINER**  
**TO THE BOARD OF APPEALS**

Mail Stop Notice of Appeal  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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TECHNOLOGY CENTER 2800

Sir:

Applicant(s) hereby appeal(s) to the Board of Appeals from the decision dated July 14,  
2003 July 14, 2003 of the Primary Examiner finally rejecting claims 1-14.

Appeal Fee: \$ \$330.00

- ☐ Not required (fee paid in prior appeal in this application).
- ☒ Charge to Deposit Account No. 500417.
- ☐ Check Enclosed.

Respectfully submitted,

10/07/2003 AWONDAF1 00000015 500417 09976341  
01 FC:1401 330.00 DA

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